Errata: A multi-function sequencer by Leigh Harrison VK6WA

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PCB Layout

Diodes SM4007A or SM4004 PCB footprint should be MELF and not SOD80 as shown on the parts list (Table 11.)
This will be corrected on the next PCB layout if there is sufficient interest.

Front Panel Wiring Diagram

S1 selects HF and/or 6 m PA
LED7 (green) should be for HF PA on.

S2 selects VHF/UHF PA
LED8 (green) should read VHF/UHF PA on.

S3 selects VHF/UHF PREAMP.

LED9 (green) should be for PREAMP on.

ACC Connector Pin-out (Figure 1.)

Pins 1 and 2 are reversed. Pin 1 should be on the right-hand side and pin 2 should be on the left-hand side of the ACC Connector (See Figure 1).

Sequencer Block Diagram (Figure 3.)

S1 should be HF PA SW. S2 is V/UHF PA SW. S3 should be PREAMP SW. (See Front Panel Wiring Diagram).

LED7 – HF PA, LED8 – V/UHF PA, LED9 – PREAMP. (See Front Panel Wiring Diagram).

Setting up the Clock (Fuse Bits)

Don't forget to select the clocking option (See Atmel Atmega 32 Data Sheet)

Assuming a 4.0 MHz clock:

External crystal resonator, high frequency, start-up time: 16k CK + 64 ms. Hex value = 0x3F

Note: The Eclipse IDE lets you change these and other settings in a user-friendly interface with AVR-dude.